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Fig. 1

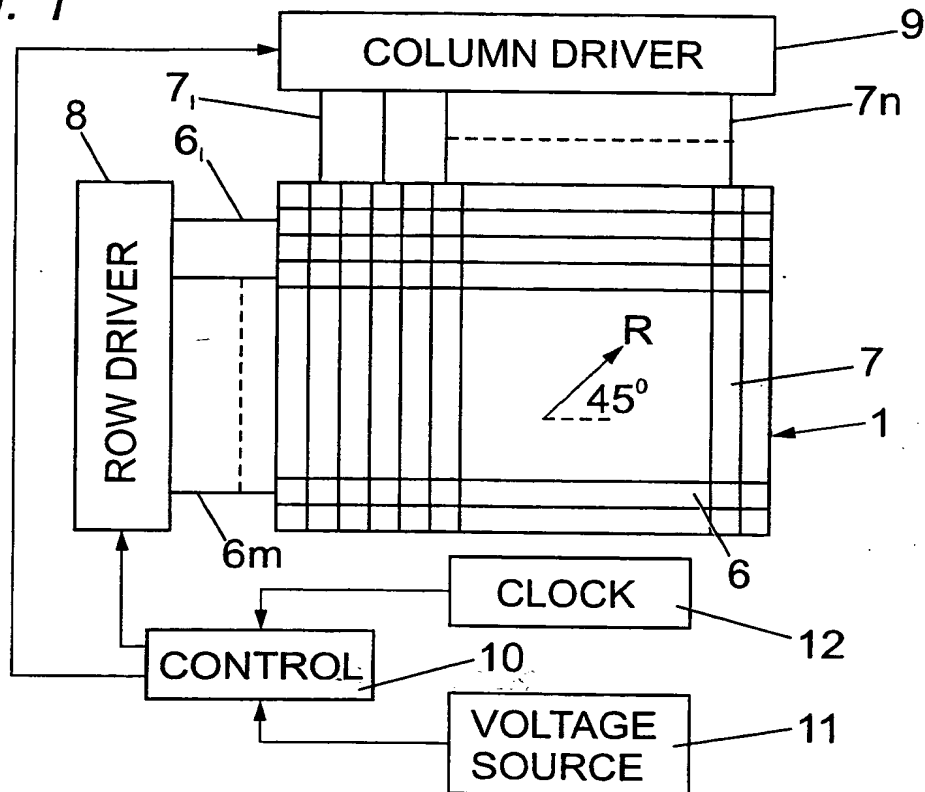
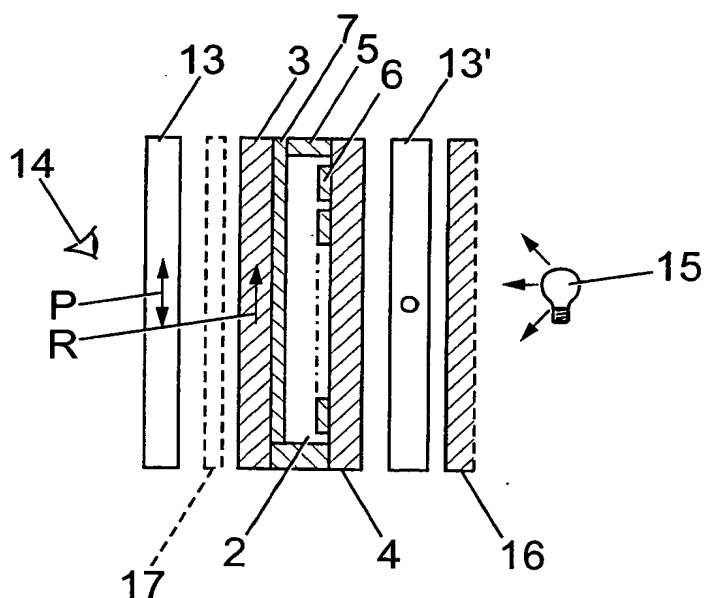


Fig. 2



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Fig. 3a

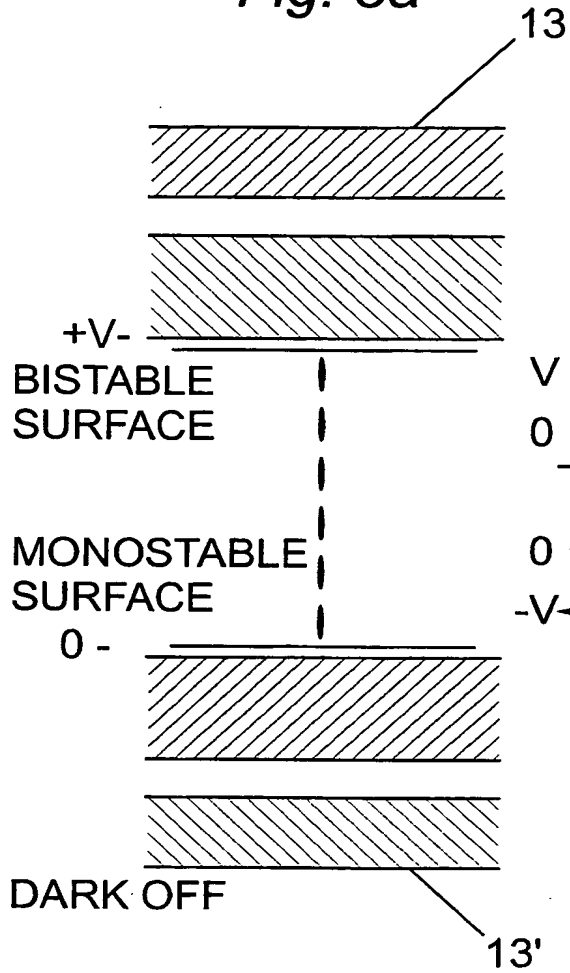
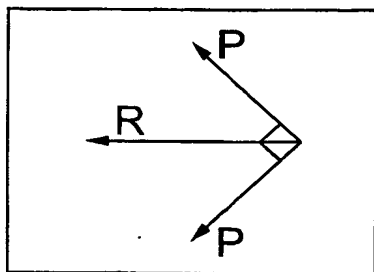
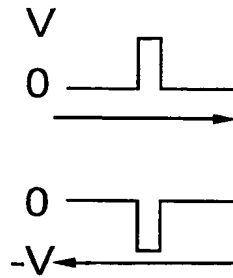
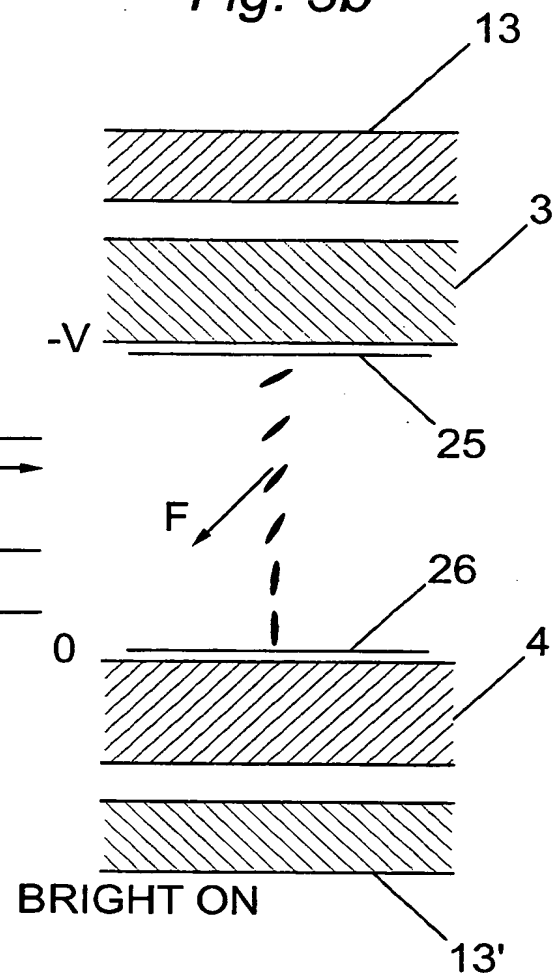


Fig. 3b



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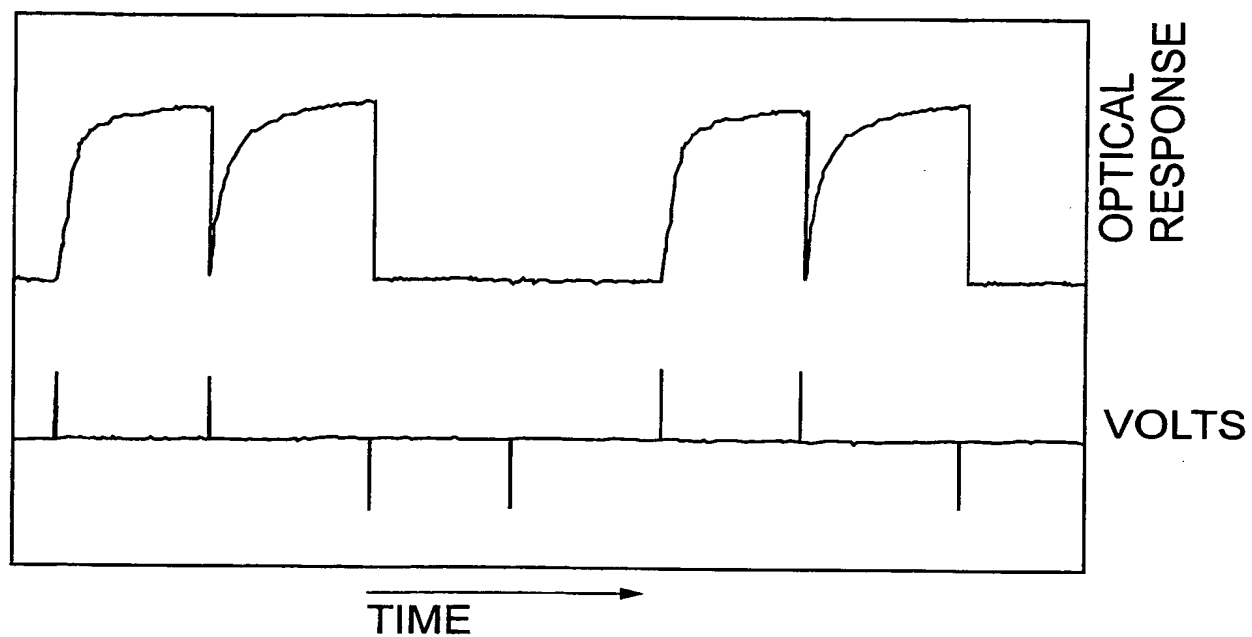


Fig. 4

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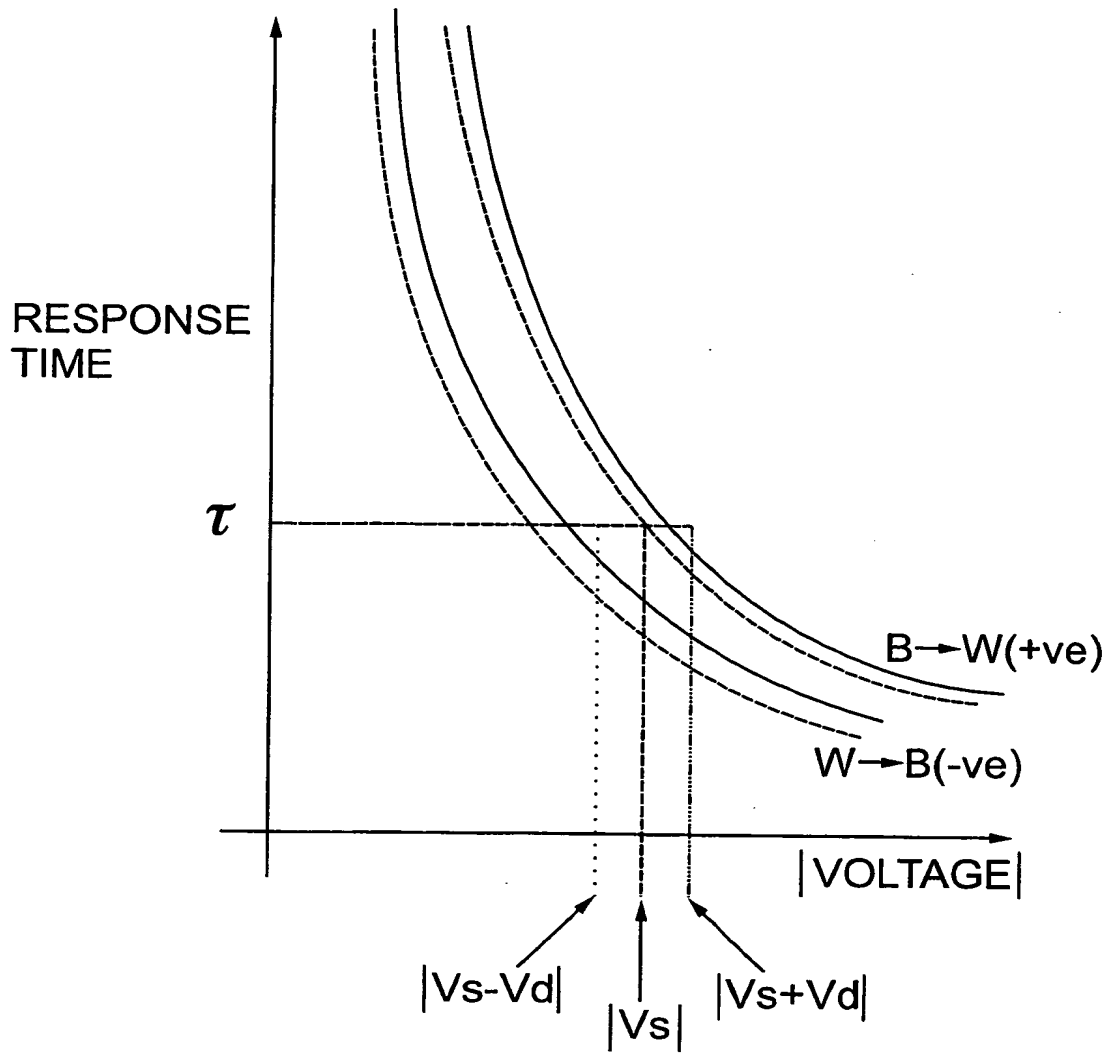


Fig. 5

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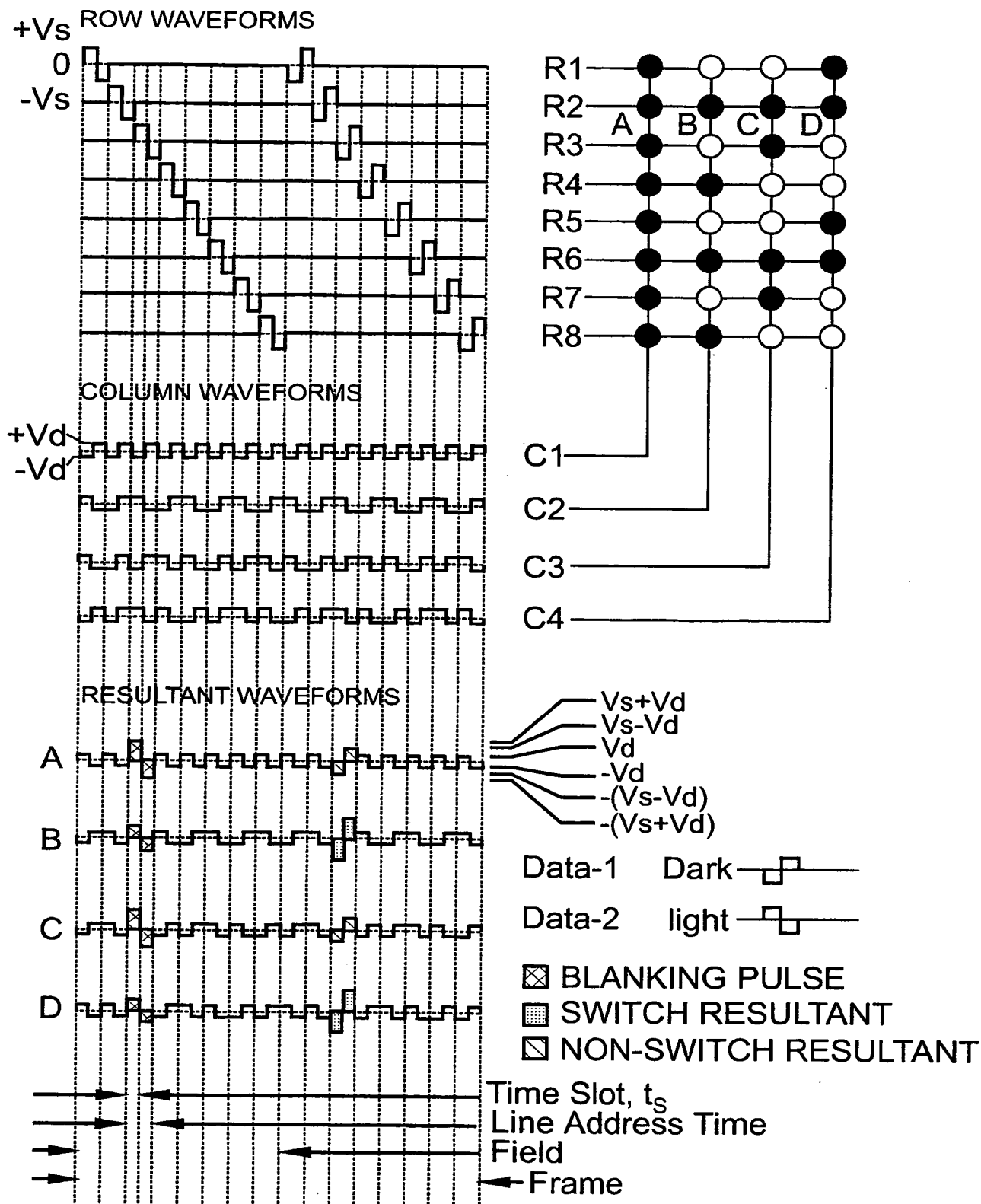


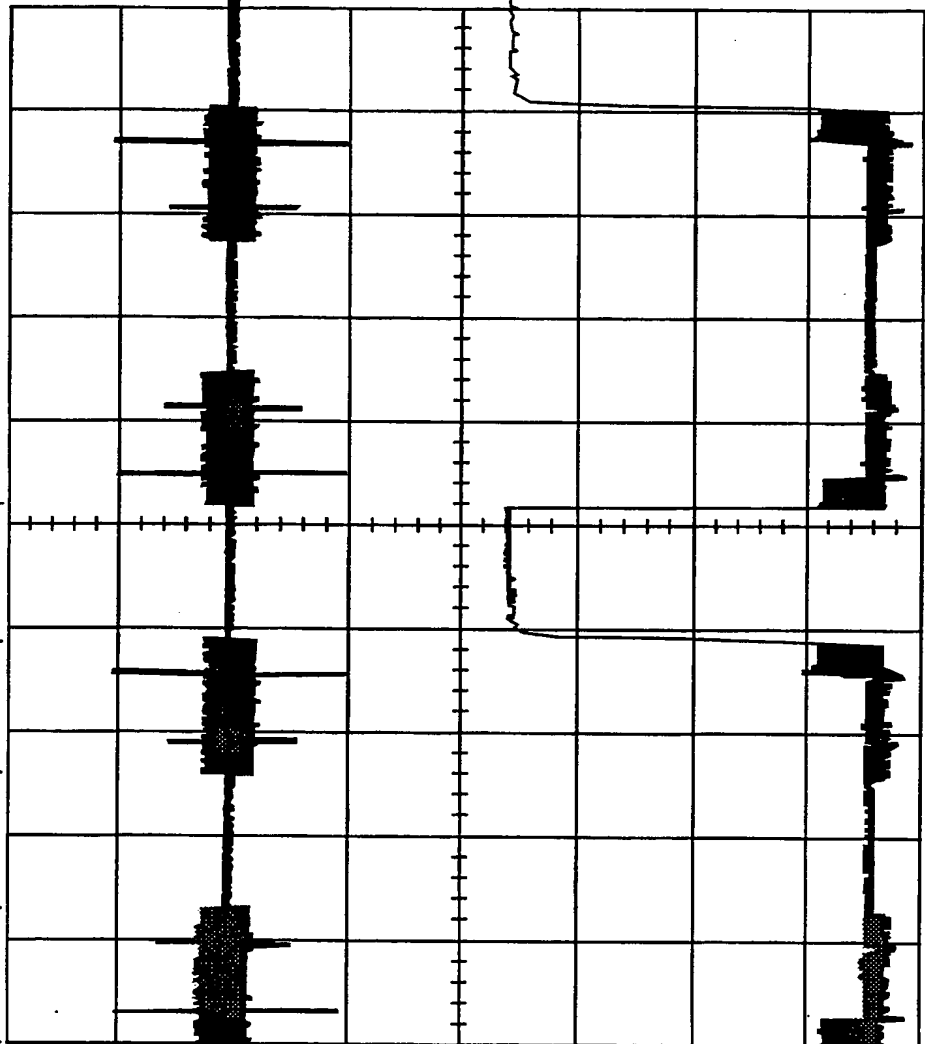
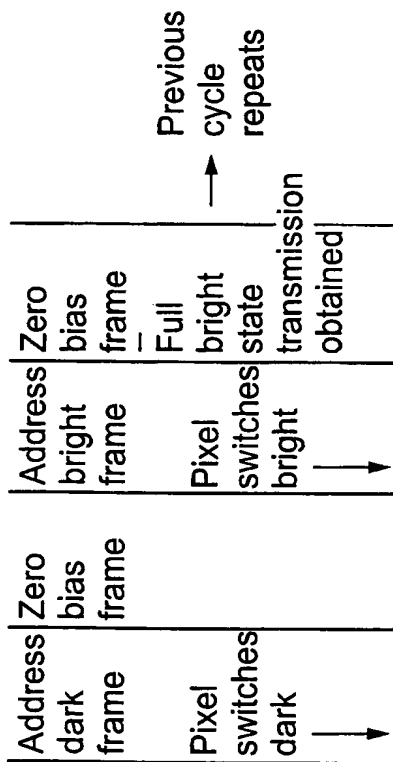
Fig. 6

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APPLIED WAVEFORM

TRANSMISSION

Fig. 7



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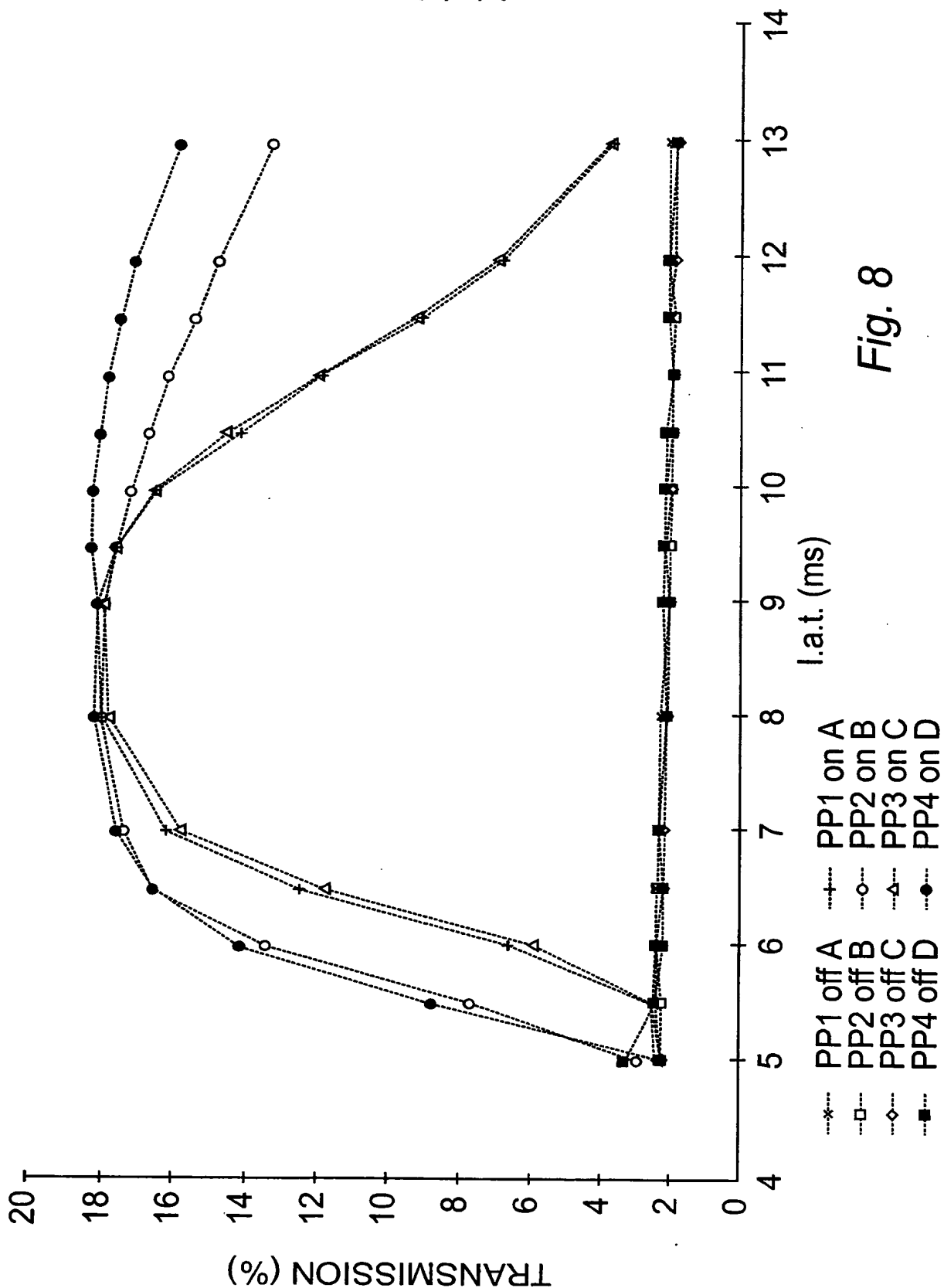


Fig. 8

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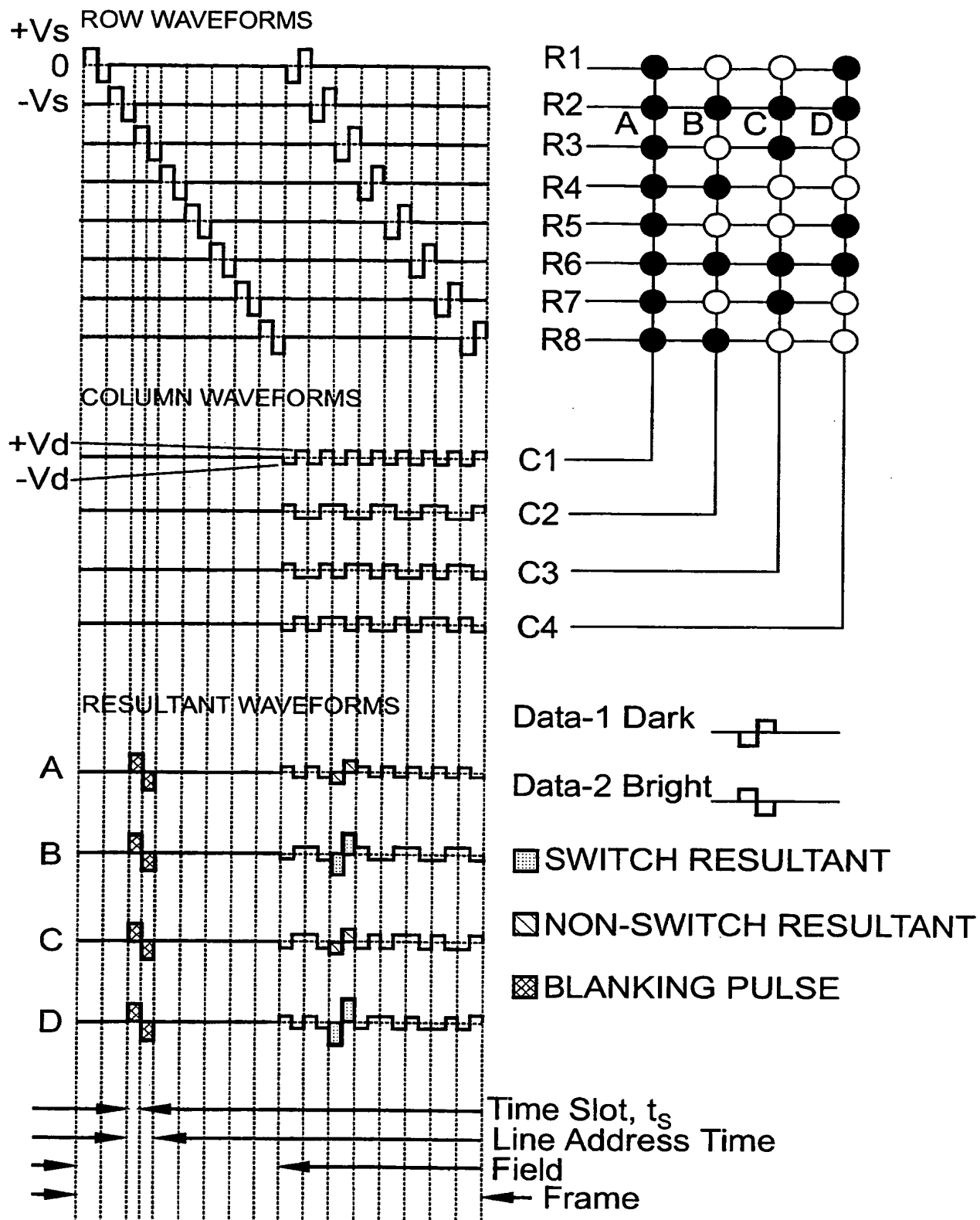


Fig. 9

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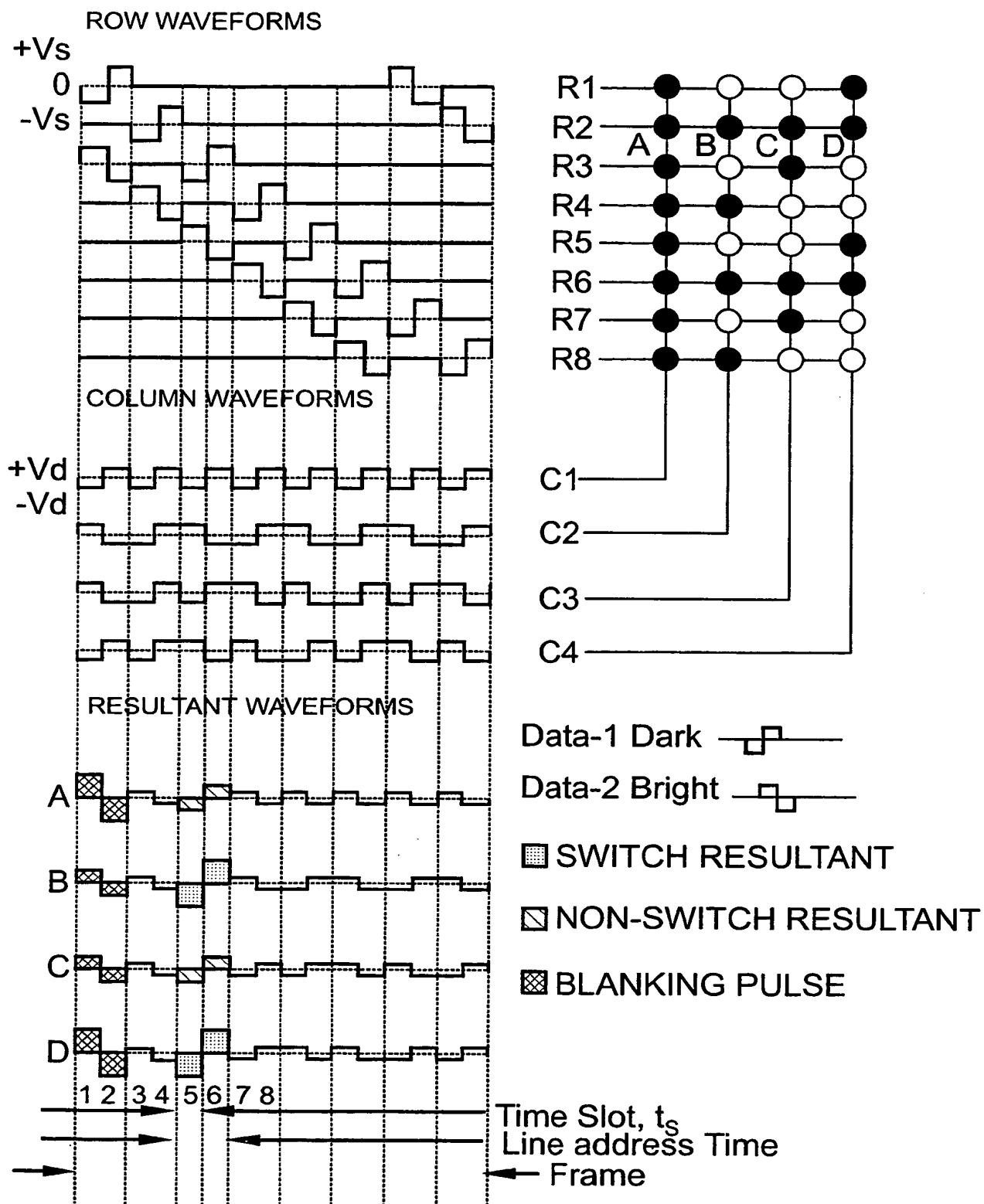


Fig. 10

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FIG. 11

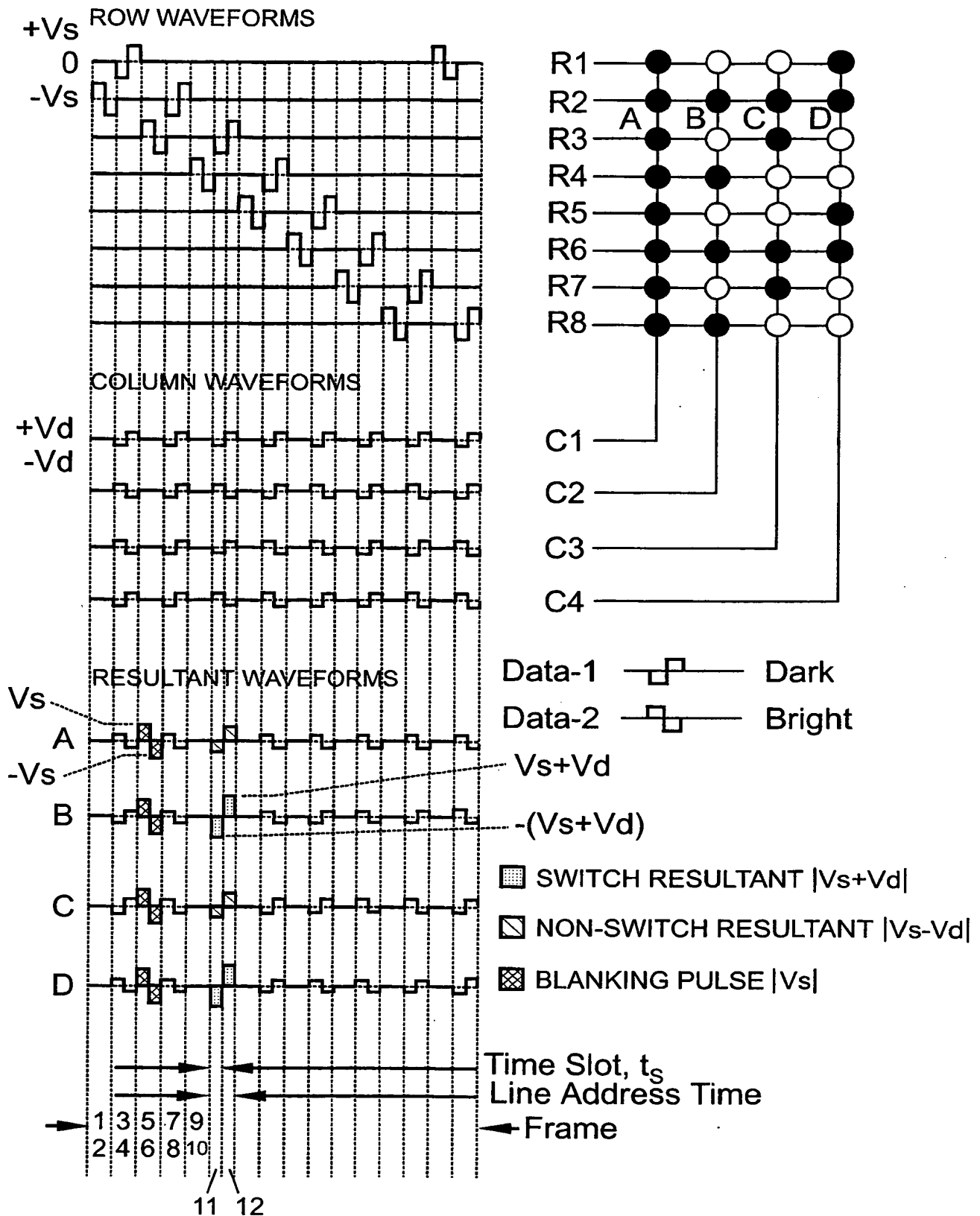


Fig. 11

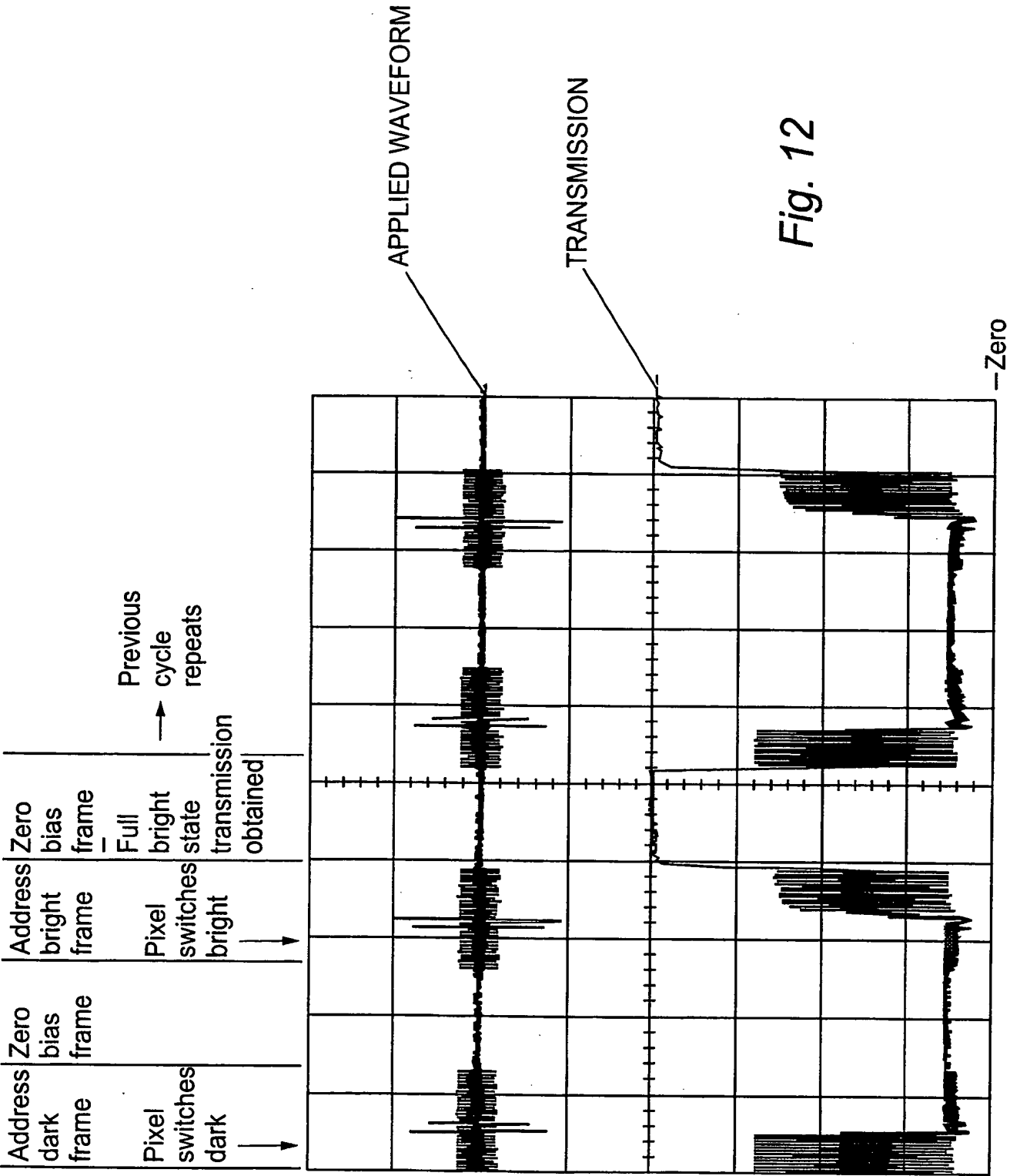


Fig. 12

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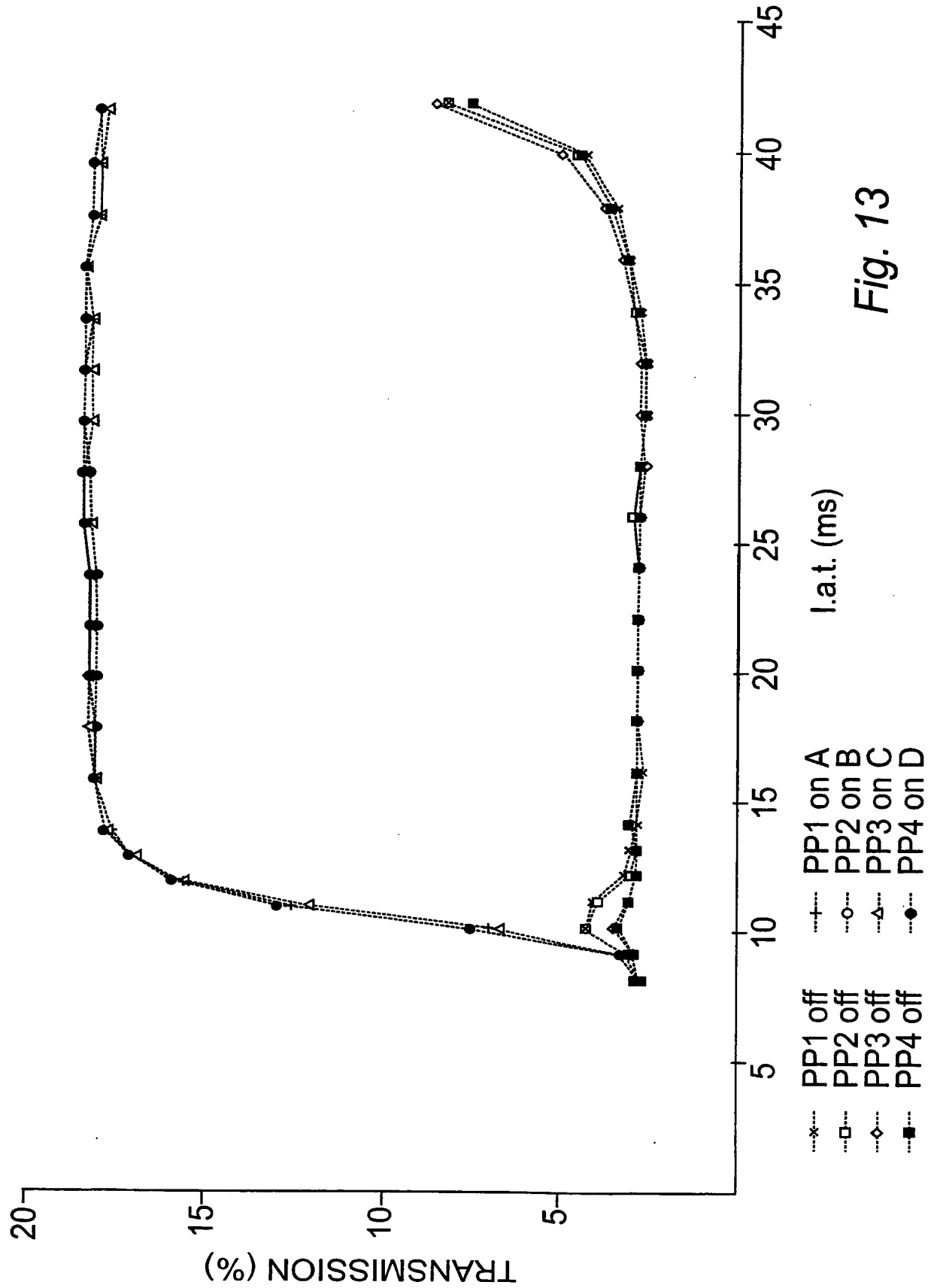


Fig. 13

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FIG. 14

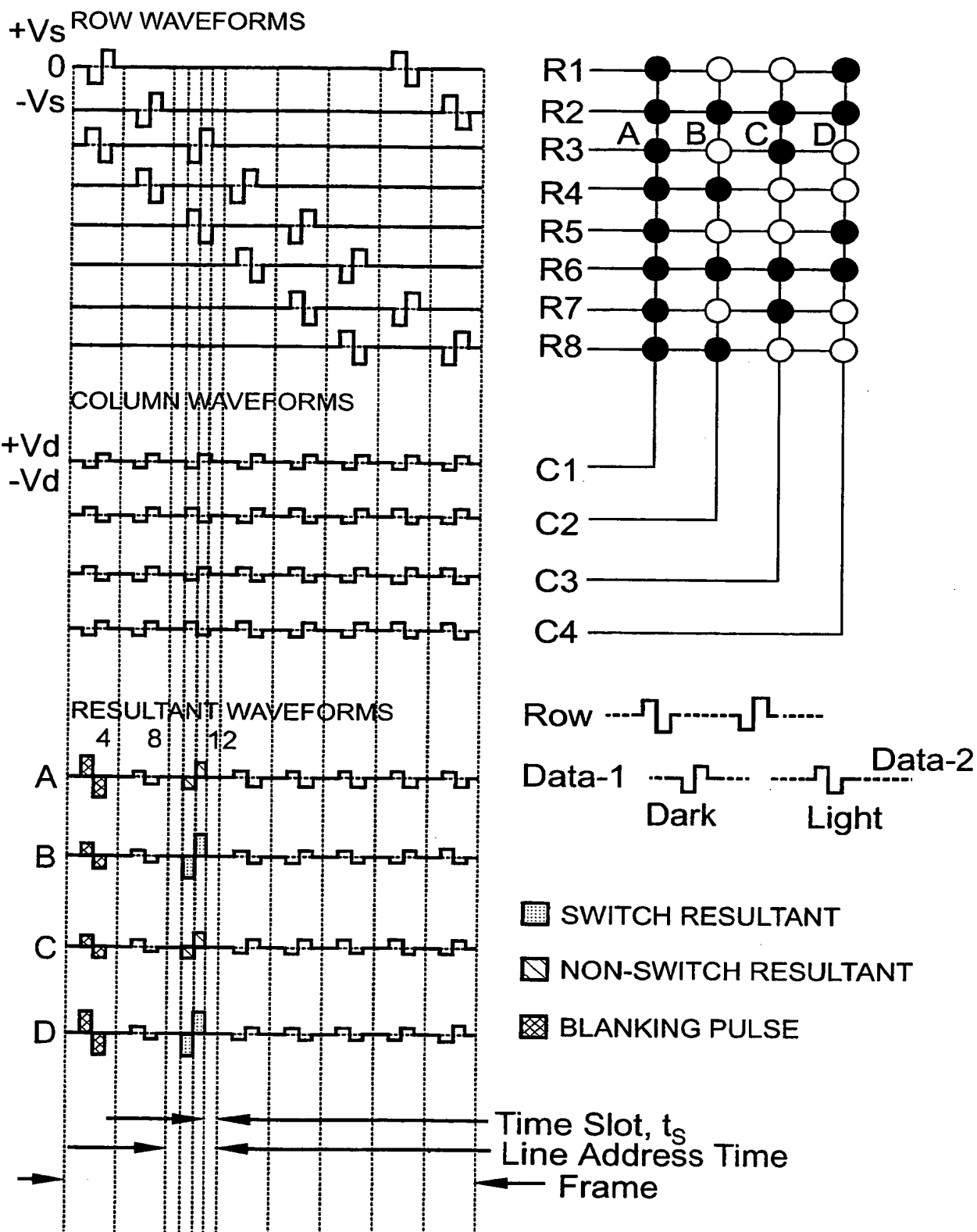
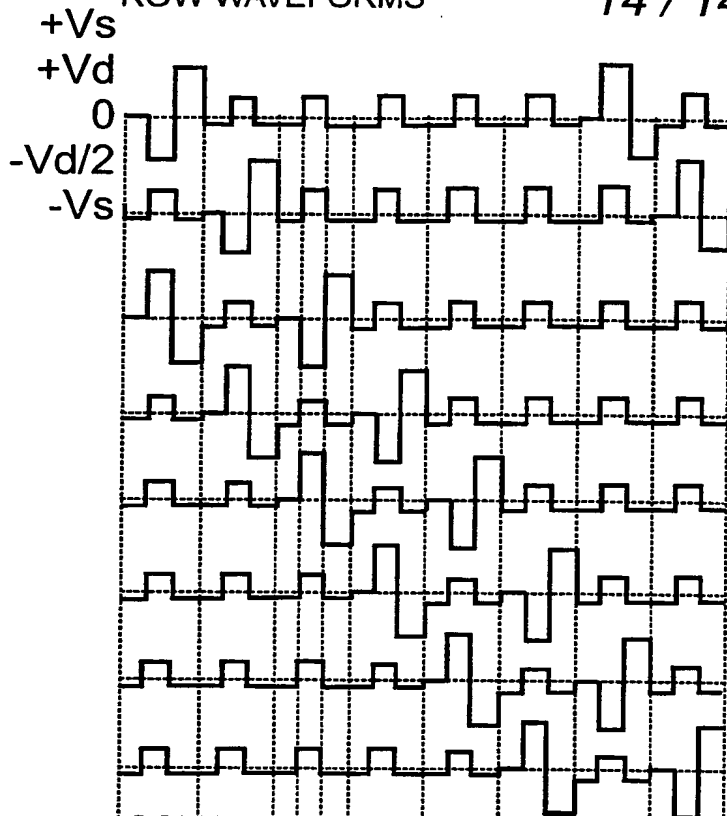


Fig. 14

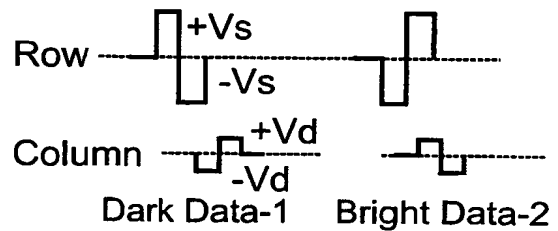
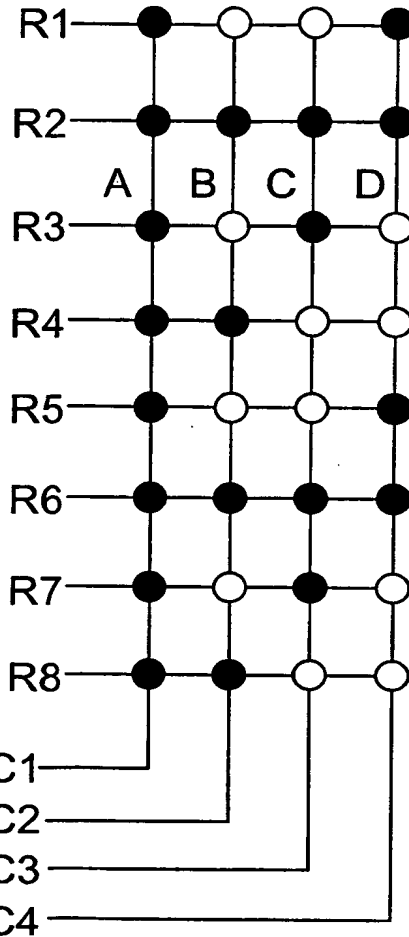
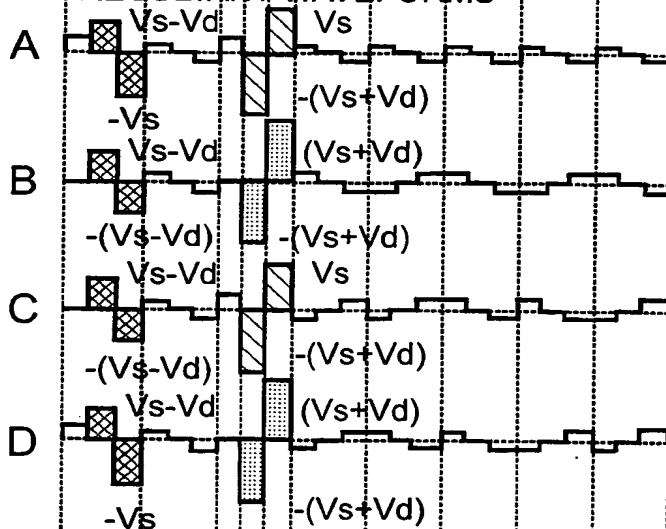
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ROW WAVEFORMS



COLUMN WAVEFORMS

RESULTANT WAVEFORMS



Voltage Reduction Waveform
 $-Vd/2$, Vd , $-Vd/2$

- SWITCH RESULTANT
- NON-SWITCH RESULTANT
- BLANKING PULSE

Time Slot, t_s
 Line Address Time
 Frame

Fig. 15